

UNITED STATES PATENT AND TRADEMARK OFFICE



UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Tracker ark Office Address COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandra, Viginia 22313-1450 www.nepto.gov

APPLICATION NO.	PLICATION NO. FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/052,652	(01/18/2002	Ronalf Kramer	1406/36 5317		
25297	7590	07/02/2003				
JENKINS &		N, PA		EXAMINER		
3100 TOWER BLVD SUITE 1400				TAN, VIBOL		
DURHAM,	NC 2770	/		ART UNIT	PAPER NUMBER	
				2819		
			DATE MAILED: 07/02/2003			

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
Advisory Action	10/052,652	KRAMER, RONALF	
navisory nodesii	Examiner	Art Unit	,
	Vibol Tan	2819	
The MAILING DATE of this communication appe	ars on the cover sheet with the o	orrespondence add	ress
THE REPLY FILED 16 June 2003 FAILS TO PLACE THE Therefore, further action by the applicant is required to average final rejection under 37 CFR 1.113 may only be either: (1) condition for allowance; (2) a timely filed Notice of Appeal Examination (RCE) in compliance with 37 CFR 1.114.	oid abandonment of this application at the control of the control	ation. A proper reply n places the applica	y to a ition in
PERIOD FOR RE	PLY [check either a) or b)]		
a) The period for reply expiresmonths from the mailing b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire to ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	dvisory Action, or (2) the date set forth ater than SIX MONTHS from the mailing	g date of the final rejection	on.
Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the feet (2) as set forth in (b) above, if checked. Any reply received by the Office timely filed, may reduce any earned patent term adjustment. See 37 C	f extension and the corresponding amo the shortened statutory period for reply be later than three months after the mai	unt of the fee. The approriginally set in the final	opriate extension Office action; or
1. A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFF	•		
2. The proposed amendment(s) will not be entered be	ecause:		•
(a) they raise new issues that would require further	er consideration and/or search (s	see NOTE below);	
(b) \square they raise the issue of new matter (see Note b	elow);	•	
(c) they are not deemed to place the application ir issues for appeal; and/or	n better form for appeal by mate	rially reducing or sir	mplifying the
(d) they present additional claims without cancelling NOTE:	ng a corresponding number of fi	nally rejected claim	S.
3. Applicant's reply has overcome the following reject	ion(s):		
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed	amendment
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for application in condition for allowance because:		dered but does NO	T place the
 The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection. 	ause it is not directed SOLELY t	o issues which were	e newly
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we			and an
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed:			
Claim(s) objected to:		·	
Claim(s) rejected: <u>1-11</u> .			
Claim(s) withdrawn from consideration:			
8. The proposed drawing correction filed on is a	a)□ approved or b)□ disapp	roved by the Exami	ner.
9. Note the attached Information Disclosure Statemen	it(s)(PTO-1449) Paper No(s)	· ·	
10.⊠ Other: <u>See Continuation Sheet</u>		- 0	
		2	_
		Vibol Tan Patent Examiner, A	u 2819

Continuation of 10. Other: Applicant's arguments regarding claims 1-11 have been considered, but they are not persuasive. Referrring t Fig. 2 of Horita, the Examiner respectifully would like to point out that "a first and second transistor (MP1, MP2) including control terminals (gate terminals) being controllably independent (the gate of MP1 being controllably independent by the output from inverter IN1 where the terminal of MP2 being controllably independent by the output from IN3). The Examiner does not refer to IN1 and IN2 as the first and second transistor, as applicant argues in the remark on page 5. Furthermore, one ordinary skill in the art would agree that the amended Figures 1A and 1B in the application are similar to Fig. 2 in Horita. The examiner respectifully believes that the cited reference teaches every claimed invention.